



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Shunichi SEKI et al.

Application No.: 10/613,081

Filed: July 7, 2003

Docket No.: 116462

For: COMPOSITION, ORGANIC CONDUCTIVE LAYER INCLUDING COMPOSITION, METHOD FOR MANUFACTURING ORGANIC CONDUCTIVE LAYERS, ORGANIC EL ELEMENT INCLUDING ORGANIC CONDUCTIVE LAYER, METHOD FOR MANUFACTURING ORGANIC EL ELEMENTS, SEMICONDUCTOR ELEMENT INCLUDING ORGANIC CONDUCTIVE LAYER, METHOD FOR MANUFACTURING SEMICONDUCTOR ELEMENTS, ELECTRONIC DEVICE, AND ELECTRONIC APPARATUS

THIRD INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. Relevance of the references is discussed in the present specification.

Respectfully submitted,

James A. Oliff
Registration No. 27,075

Michael Britton
Registration No. 47,260

JAO:MB/gam

Date: November 6, 2003

OLIFF & BERRIDGE, PLC
P.O. Box 19928
Alexandria, Virginia 22320
Telephone: (703) 836-6400

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Sheet 1 of 1

Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 116462		APPLICATION NO. 10/613,081	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				APPLICANT(S) Shunichi SEKI et al.			
				FILING DATE July 7, 2003		GROUP	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
	1	FURUSAWA et al., "Late-News Paper: Inkjet-Printed Bus and Address Electrodes for Plasma Display", <i>SID 02 DIGEST</i> , 2002, pp. 753-755.					
	2	SIRRINGHAUS et al., "High-Resolution Inkjet Printing of All-Polymer Transistor Circuits", <i>SCIENCE</i> , Vol. 290, December 15, 2000, pp. 2123-2126.					
EXAMINER					DATE CONSIDERED		
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Date: November 6, 2003